

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Chiou et al. Application Serial No.: 10/040,233

Filed: November 7, 2001

For: Novel Design and Fabrication Method for Finger N-Type Doped

Photodiodes with High Sensitivity for CIS Products

Patent No.: Issue Date:

Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

CERTIFICATE UNDER 37 C.F.R. §3.73(b) ESTABLISHING RIGHT OF ASSIGNEE TO TAKE ACTION

1. The assignee of the entire right, title and interest hereby seeks to take action in the PTO in this matter.

IDENTIFICATION OF ASSIGNEE

2. The assignee of this matter is:

TAIWAN SEMICONDUCTOR MANUFACTURING CO., LTD.

8, Li-Hsin Rd. 6 Hsinchu Science Park Hsinchu, Taiwan 300-77, R.O.C.

PERSON AUTHORIZED TO SIGN

3. Daniel R. McClure Attorney for Assignee

4	A abai	n of title from the inventor(s) to the current assignee is shown below:			
4.	A chai	n of the from the inventor(s) to the current assigned is shown below.			
	a.	From: Yu-Zung Chiou, Kuen-Hsien Lin, Chen Ying Lieh, Shou-Yi Hsu To: Taiwan Semiconductor Manufacturing Co., Ltd. Recorded in PTO: Reel: Frame:			
	b.	From: To: Recorded in PTO: Reel: Frame:			
		Recorded in PTO: See Attached evidencing assignment that has already been filed. No Reel and Frame has been assigned as of yet.			
		DECLARATIONS			
5.	I, the u	indersigned, have reviewed all the documents in the chain of title of the			
		application patent			
matter identified abo		eve and, to the best of my knowledge and belief, title is in the assignee			
these statementare punishabl	atement of this are reduced are	by declare that all statements made herein of my own knowledge are true, its made on information and belief are believed to be true; and further, that made with the knowledge that willful false statements, and the like so made, ne or imprisonment, or both, under Section 1001, Title 18 of the United such willful false statements may jeopardize the validity of the application thereon.			
7. behalf of the a		person signing below, aver that I am empowered to sign this statement on e.			
		Dallha			
Tel. No. 770-	033 05	Daniel R. McClure, Reg. No. 38,962			
Customer No.					
D	50 044	Atlanta, Georgia 30339-5948			
Docket No. 2:	52016-1	USU			

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TS01-102 To the Honorable Commissioner of 1 102525071 I. Name of conveying party(ies): YU-ZUNG CHIOU, KUEN-HSIEN LIN, CHEN YING LIEH, SHOU-YI HSU Additional name(s) or conveying party(ies) attached? Additional name(s) or conveying party(ies) attached? Additional name(s) or conveying party(ies) attached? Test Address: 3. Nature of conveyance X Assignment Merger Security Agreement Change of Name Company Internal Address: All6 1 1 2003 Street Address: 121 Park Ave. 3 Science-Based Industrial Park City: Hsin-Chu Zip: Country: Taiwan, R.O.C.	FOPM PTO~1595	 08-14-20	03	EL End त	U.S. DEPT. OF COMP Patent and Tradema	MERCE ark
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Name: Taiwan Semiconductor Manufacturing Company		
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Rd. 6 Industrial Park van 300-77		
address attached? Yes X No		
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FEBRUARY 27, 2004

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SAUNDRA BALLENGER, EXAMINER ASSIGNMENT DIVISION OFFICE OF PUBLIC RECORDS

	
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ASSIGNMENT	r of invention
In consideration of the payment by ASSIGNE receipt of which is hereby acknowledged, and	E to ASSIGNOR of the sum of One Dollar (\$1.00), the d for other good and valuable consideration,
ASSIGNOR:	
Type or print name of ASSIGNOR SF. No. 15. Alley So Lane 142, Address Sec. L. Luostu Rol, Taipei, Taiwan III Republic of China Nationality	KUEN-HSIEN LIN Type or print name of ASSIGNOR No.2, Gungkuan Rd., Wantuan Shiang, Address Pingtung Taiwan Republic of China Nationality
Chen Ying Lieh Type or print name of ASSIGNOR 1171-2, No. 88, Sec. 1, Junghua E. Rd. Address Dung Chiu, Tainan, Taiwan 701, R.O.C. Nationality	SHOU-YI HSIU Type or print name of ASSIGNOR 5F13, No. 66, Minde Rd., Judung Jen, Address Hsinchy, Taiwan Republic of China Nationality
ASSIGNEE:	
Taiwan Semiconductor Manufacturing Co 121 Park Ave. 3 Science-Based Industrial Park Hsin-Chu, Taiwan, R.O.C. Taiwan, R.O.C.	o. Ltd.
and the successors, assigns and legal repres	·
<u> </u>	one of the following)
) interest for the United States and its territorial possessions gn rights are also to be assigned)
in and to any and all improvements which are	e disclosed in the invention entitled:

For X U.S. and/or X Foreign Rights

A Novel Design And Fabrication Method For Finger N-Type Doped Photodiodes With High Sensitivity For CIS Products

and which is	found	in
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(a) X U.S. patent application executed on even d	ate herewith			
	U.S. patent application executed on			
	filed on			
(d) U.S. patent no.	issued			
	rign application(s) is also being assigned)			
(e) and any legal equivalent thereof in a foreign	n country, including the right to claim priority			
and, in and to, all Letters Patent to be obtained for continuation, division, renewal, or substitute, thereof, and thereof.				
ASSIGNOR hereby covenants that no assignment be made or entered into which would conflict with this	t, sale, agreement or encumbrance has been or will assignment.			
pertinent facts and documents relating to said invention may be known and accessible to ASSIGNOR and will be proceeding related thereto and will promptly execute an any and all papers, instruments or affidavits required to application, said invention and said Letters Patent and or desirable to carry out the purposes thereof. IN WITNESS WHEREOF, I / We have signed this	estify as to the same in any interference, litigation or and deliver to ASSIGNEE or its legal representatives to apply for, obtain, maintain, issue and enforce said and said equivalents thereof which may be necessary			
<u>Oct. 8, 2001</u> (Date of				
WARNING Date of signing must be the same as the date of execution	cution of the application if item (a) was checked above.			
	Signature or ASSIGNOR(S) Kuln-HSTeir XIn Chen Ying dieh			
	Chan Jing dieh			
	Show - y. Hall			
•	Sicon Je New			
	•			
If ASSIGNOR is a legal entity complete the following inform	nation -			
	·			
	Type or print the name of the above person authorized to sign on			
	behalf of ASSIGNOR			
	·			
	Title			
Note: No witnessing, notarization or legalization is necessary, if the assi	ignment is notarized or legalized then it will only be prima facie evidence (35 USC			
261). Use next page if notarization is desired.				
	Notarization of Legalization Page Added.			

Commissioner for patents Washington, D.C. 20231

ON THE ATTORNEY BY ASSIGNEE OF ENTIRE INTEREST

As assigned of record of each of the patent applications listed in the table of attachment A,

REVOCATION OF PRIOR POWERS OF ATTORNEY

all powers of attorney previously given in each of the listed patent applications are hereby revoked, and

NEW POWER OF ATTORNEY

the following attornoys/agents are hereby appointed to prosecute this application and to transact all business in the Patern and Trademark Office connected therewith. I bereby appoint all attorneys of Thomas, Kayden, Horstemeyer & Risley, LLP, who are listed under the USPTO Costoner Number shown below as the attorneys to prosecute this application and to transact all business on the United States Patent and Trademark Office connected therewith, recognizing that the specific attorneys listed under that Castoner Number may be changed from time to time at the sole discretion of Thomas, Kayden, Horstemeyer & Risley, LLP, and request that all correspondence about the application be addressed to the address filed under the same USPTO Customer Number.

24504 Patent Trademark Office

Please direct all future correspondence and telephone calls to:

May 24, 2004

Daniel R. McClure, Reg. No. 38,962 THOMAS, KAYDEN, HORSTEMEVER & RISLEY, L.J. P. 100 Galleria Parkway, Suite 1750 Atlanta, Georgia, 30339 770-933-9500

ASSIGNEE OF ENTIRE INTEREST

TAIWAN SENICONDUCTUR MANUFACTURING COMPANY, LTD.

8, Li-Hsin Rd. 6 Hsinchu Science Park Uşınchu, Taiwan 300-77, R.Q.C

ASSIGNEE CERTIFICATION

The certification under 37 C.F.R. §5.73(b) establishing the right of assignce to take action is attached hereto along with documentation evidencing same. Further, in my official position with Taiwan Semiconductor Manufacturing Company, Ltd., I am authorized to sign documents and otherwise act on its behalf in connection with the management and handling of patent applications and other intellectual property matters.

Date

Chien-Wei (Chris) Chau

Dhector - Intellectual Property Division

No	Serial No	TSMC No.	Application Title	Filing Date	Assignment (Recl/Frame)	
givernet «	10/120,834	TS1999705C	Process For High Voltage Oxide And Select Gate Poly For Split-Gate Flash Memory	4/11/2002	012791/0621	
2	10/074,882	TS2000885	Methodology To Reduce The Early Failure Rate By Applying Dynamic Voltage Stressing In Testing With Screen Criteria Of Delta LSB	2/12/2002	012599/0107	
,	09/933,961	TS20010325	Electrostatic Discharge-Free Container For Insulating Articles	8/22/2001	012128/0489	
4	10/058,473	TS20011063	Enhanced Adhesion Strength Between Mold Resin And Polyimide	1/28/2002	014216/0356	
5	10/040,233	TS20010102	Novel Design And Fabrication Method For Finger N-Type Doped Photodiodes With High Sensitivity For CIS Products	11/7/2001	Filed 3-29-04 Copy attached	
6	10/225,803	TS20010389	HDP Gap-Filling Process For Structures With Extra Step At Side- Wall	8/22/2002	013228/0267	
7	10/679,737	TS20011380B	Method For Forming A Novel Top- Metal Fuse Structure	10/6/2003	012593/0150	
8	10/437,092	TS20010996B	Horizontal Surrounding Gate MOSFETS	5/13/2003	013308/0785	
9	10/338,138	TS20011442	Integrated High Performance MOS Tunneling Led In ULSI Technology	1/8/2003	013647/0832	
10	10/177;912	TS20011051	Structure And Melhod For Low- Stress Concentration Solder Bumps	6/20/2002	013041/0230	
11	10/313,501	TS20011509	Multivariate RBR Tool Aging Detector	12/6/2002	Copy attached	
12	09/932,680	TS1998850/85 2BCC	Tilt-Angle ton Implant To Improve Junction Breakdown in Flash Memory Application	8/20/2001	010367/0646	
13	09/905,408	TS20010133	Selective Formation Of Metal Gate For Dual Fate Oxide Application	7/16/2001	012011/0342	
14	10/288,194	T\$20020083	Self-Aligned Structure With Unique Erasing Gate In Split Gate Flash	11/8/2002	013483/0576	
15	10/255,482	TS20020058	Method To Prevent Side Lobe On Seal Ring	9/26/2002	013341/0450	
16	10/245,433	TS20011294	Metal Fuse For semiconductor devices	9/17/2002	a a servición de la manda de la dela del minimo de la dela del como esta de la dela del como esta de	

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